



EXPEDITED PROCEDURE – EXAMINING GROUP 2811

S/N 09/945,495

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Fernando Gonzalez	Examiner:	Douglas W Owens
Serial No.:	09/945,495	Group Art Unit:	2811
Filed:	August 30, 2001	Docket No.:	303.776US1
Title:	VERTICAL TRANSISTOR AND METHOD OF MAKING		

---

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**

**MAR 08 2004**

In response to the Final Office Action mailed December 30, 2003, please amend the application as follows:

Okay to  
enter  
DLV